



1117.69877

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Akito Hara
Serial No.: 10/790,875
Conf. No.: 4718
Filed: 3/2/2004
For: SEMICONDUCTOR DEVICE AND
METHOD OF FABRICATING THE
SAME
Art Unit: 2811
Examiner: Not assigned

I hereby certify that this paper is being deposited with the United States Postal Service as FIRST-CLASS mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this date.

2 June 04
Date

[Signature]
Registration No. 29,367
Attorney for Applicant(s)

INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This IDS is submitted under 37 C.F.R. §1.97(b) within either of the following time periods, whichever occurs last:

- (a) within three months of either the filing date of the national application or the date of entry into the national stage; or
- (b) before the mailing date of first office action on the merits (i.e., not including actions such as restriction requirements).

Applicant(s) submit herewith Form PTO-1449 (Information Disclosure Citation) together with copies of foreign patents, publications or other information of which applicant(s) are aware, which applicant(s) believe may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 C.F.R. §1.56. Applicant(s) respectfully submit that the citation of any reference on Form PTO-1449 does not constitute an admission that the reference qualifies as prior art.

It is requested that the information disclosed on the enclosed Form PTO-1449 be made of record in this application.

The Commissioner is hereby authorized to charge any additional fees which may be required to this application under 37 C.F.R. §§1.16-1.17, or to credit any overpayment, to Deposit Account No. 07-2069. A duplicate copy of this sheet is enclosed.

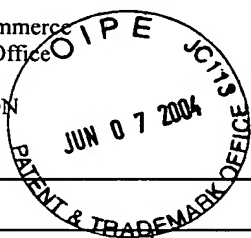
Respectfully submitted,

GREER, BURNS & CRAIN, LTD.

Bx: *[Signature]*
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Form PTO-1449 (Rev. 8-88)	U.S. Department of Commerce Patent and Trademark Office	Attorney Docket No.: 1117.69877	Serial No.: 10/790,875
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)		Applicant: Akito Hara	
		Filing Date: 3/2/2004	Group: 2811



U.S. PATENT DOCUMENTS

Examiner Initial*	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	2002/0031876	Mar. 14, 2002	Hara et al.	438	166	8-22-01

FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
	10-173192	Jun. 26, 1998	Japan			Abs. only	
	2002-033481	Jan. 31, 2002	Japan			Abs. only	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	Makihira et al.; Double-Gate Poly-Si Thin-Film Transistors Fabricated Using Self-Aligned Technology; Am-LCD '01; Digest of Technical Papers; July 11-13, 2001

Examiner	Date Considered
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*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.